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XA-9630

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Tsuyoshi FUJIWARA et al.

Appln. No.: 10/083,416

Group Art Unit: 2813

Filed: February 27, 2002

Examiner: Thang Nguyen

For: METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE COMPRISING A MEMORY CELL AND A
CAPACITOR

* * *

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

In response to the Office Action mailed August 20,
2003, please amend the above-identified patent
application as indicated below.

Amendments to the claims are reflected in the listing
of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 12 of this paper.

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